This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. A method comprising:

connecting the output of at least two driver circuits to a resistive network, wherein the output signals from the at least two driver circuits are combined through the resistive network to produce a resultant signal; and

configuring the resistive network and the at least two driver circuits such that the resultant signal is provided to a first node of the resistive network but not to a second node of the resistive network.

- 2. The method as recited in Claim 1, wherein the resultant signal includes an attenuated version of at least one of the output signals.
 - 3. The method as recited in Claim 1, further comprising:

coupling an input/output node of an external circuit to the first node of the resistive network, the external circuit being configured to receive the resultant signal and output an external signal; and

coupling an input node of a receiver circuit to the second node of the resistive network.

4. The method as recited in Claim 3, further comprising:

simultaneously providing the resultant signal to the external circuit and the external signal to the receiver circuit, bi-directionally through a connector coupling the resistive network to the external circuit.

- 5. The method as recited in Claim 4, wherein the external circuit includes a device under test (DUT).
- 6. The method as recited in Claim 5, wherein the device under test (DUT) includes an integrated circuit.
- 7. The method as recited in Claim 4, wherein the at least two driver circuits and the receiver circuit are part of an automated test equipment (ATE) device.
- 8. The method as recited in Claim 7, wherein the resistive network is part of the automated test equipment (ATE) device.
- 9. The method as recited in Claim 7, wherein the resistive network is included in a load board coupled to the external circuit and the automated test equipment (ATE) device.
 - 10-27. (Cancelled)